



MSH080120M1 / MSK080120M1

# MSH080120M1 / MSK080120M1

## Silicon Carbide Power MOSFET N-Channel Enhancement Mode

### Benefits

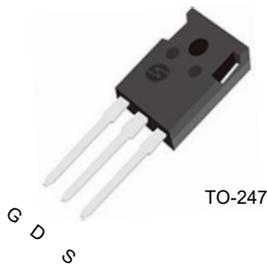
- Higher System Efficiency
- Reduced Cooling Requirements
- Increased Power Density
- Increased System Switching Frequency

### Features

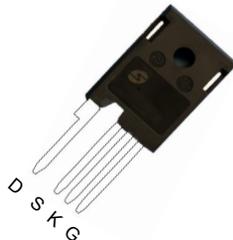
- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitances
- Easy to Parallel and Simple to Drive
- Avalanche Ruggedness
- Halogen Free, RoHS Compliant

### Applications

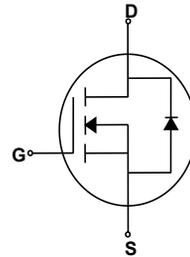
- Solar Inverters
- Switch Mode Power Supplies
- High Voltage DC/DC Converters
- IBattery Chargers
- Motor Drives
- Pulsed Power applications



TO-247



TO-247-4L



### Absolute Maximum Ratings

$T_c = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	MSH080120M1 / MSK080120M1	Units
$V_{DS\text{max}}$	Drain-Source Voltage, $V_{GS}=0\text{V}, I_D=100\mu\text{A}$	1200	V
$I_D$	Continuous Drain Current - $V_{GS}=20\text{V}, (T_c = 25^\circ\text{C})$ - $V_{GS}=20\text{V}, (T_c = 100^\circ\text{C})$	28	A
		20	A
$V_{GS\text{max}}$	Gate-Source Voltage, Absolute maximum values	-10/+25	V
$V_{GS\text{op}}$	Gate-Source Voltage, Recommended operational values	-5/+20	V
$P_D$	Power Dissipation ( $T_c = 25^\circ\text{C}$ ) $T_J=150^\circ\text{C}$	166	W
$T_J, T_{STG}$	Operating Junction and Storage Temperature	-55 to +150	$^\circ\text{C}$
$T_L$	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300	$^\circ\text{C}$

\* Drain current limited by maximum junction temperature.

### Thermal Characteristics

Symbol	Parameter	Typ	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.75	$^\circ\text{C}/\text{W}$
$R_{\theta JS}$	Thermal Resistance, Case-to-Sink Typ.	--	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	35	$^\circ\text{C}/\text{W}$

## Electrical Characteristics

$T_C = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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### Off Characteristics

$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 100\ \mu\text{A}$	1200	--	--	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 1200\text{ V}, V_{GS} = 0\text{ V}$	--	2	100	$\mu\text{A}$
$I_{GSSF}$	Gate-Source Leakage Current	$V_{GS} = 20\text{ V}, V_{DS} = 0\text{ V}$	--	50	200	nA

### On Characteristics

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{DS} = 5\text{mA}, T_C = 25^\circ\text{C}$	1.8	2.25	3.0	V
		$V_{DS} = V_{GS}, I_{DS} = 5\text{mA}, T_C = 175^\circ\text{C}$	-	1.60	-	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 20\text{ V}, I_D = 20\text{A}, T_C = 25^\circ\text{C}$	--	80	95	m $\Omega$
		$V_{GS} = 20\text{V}, I_D = 20\text{A}, T_C = 150^\circ\text{C}$		118		m $\Omega$
$g_{fs}$	Forward Transconductance	$V_{GS} = 20\text{ V}, I_D = 20\text{A}, T_C = 25^\circ\text{C}$	--	5.6	--	S
		$V_{GS} = 20\text{V}, I_D = 20\text{A}, T_C = 150^\circ\text{C}$	--	5.8	--	S

### Dynamic Characteristics

$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 1000\text{ V}, f = 1\text{MHz}$ $V_{AC} = 25\text{ mV}$	--	1710	--	pF
$C_{oss}$	Output Capacitance		--	54	--	pF
$C_{rss}$	Reverse Transfer Capacitance		--	37	--	pF

### Switching Characteristics

$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 800\text{V}, V_{GS} = -5/20\text{ V}$ $I_D = 20\text{A}, R_{G(ext)} = 5\ \Omega,$ $R_L = 40\ \Omega, \text{Timing relative to } V_{DS}$	--	23	--	ns
$t_r$	Turn-On Rise Time		--	60	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	17	--	ns
$t_f$	Turn-Off Fall Time		--	12	--	ns
$Q_g$	Total Gate Charge	$V_{DD} = 800\text{ V}, I_D = 20\text{A},$ $V_{GS} = -5/20\text{ V}$	--	85	--	nC
$Q_{gs}$	Gate-Source Charge		--	23	--	nC
$Q_{gd}$	Gate-Drain Charge		--	26	--	nC
$R_{G(int)}$	Internal Gate Resistance	$f = 1\text{ MHz}, V_{AC} = 25\text{mV}$	-	2.8	-	$\Omega$
$E_{ON}$	Turn-On Switching Energy	$V_{DS} = 800\text{V}, V_{GS} = -5/20\text{V}, I_D = 20\text{A},$ $R_{G(ext)} = 5\ \Omega, L = 142\ \mu\text{H}$		180		$\mu\text{J}$
$E_{OFF}$	Turn-Off Switching Energy			70		

### Drain-Source Diode Characteristics and Maximum Ratings

$I_S$	Continuous Drain-Source Diode Forward Current, $T_C = 25^\circ\text{C}$	-	15	-	A	
$V_{SD}$	Drain-Source Diode Forward Voltage	$V_{GS} = -5\text{V}, I_{SD} = 10\text{ A}, T_J = 25^\circ\text{C}$	--	7.9	-	V
		$V_{GS} = -5\text{V}, I_{SD} = 10\text{ A}, T_J = 150^\circ\text{C}$	-	7.0	-	

### Typical Characteristics

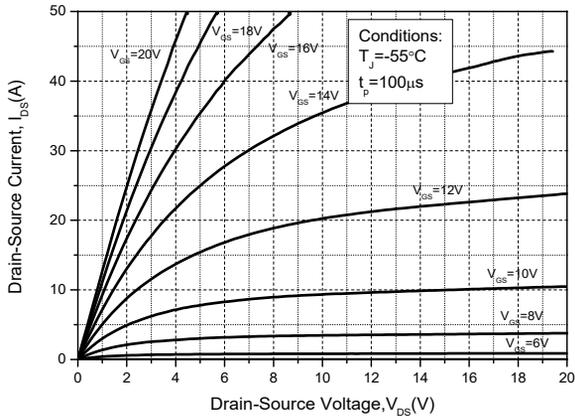


Figure 1. On-Region Characteristics  $T_J = -40^\circ\text{C}$

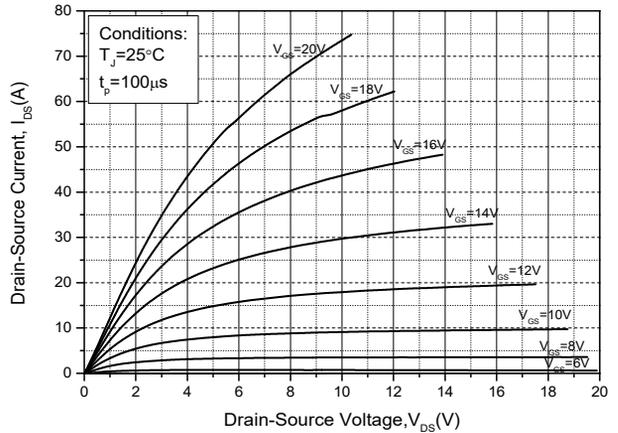


Figure 2. Output Characteristics  $T_J = 25^\circ\text{C}$

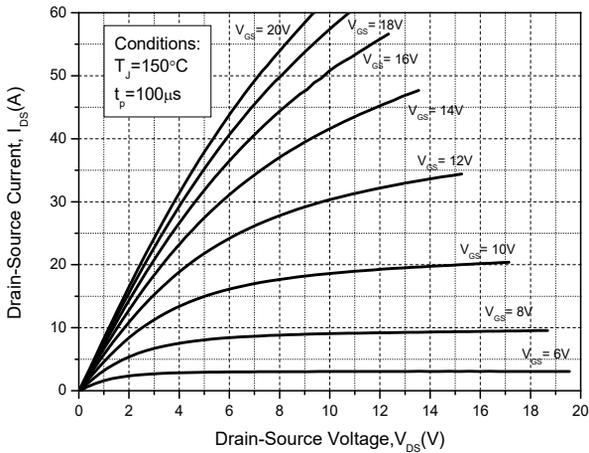


Figure 3. Output Characteristics  $T_J = 175^\circ\text{C}$

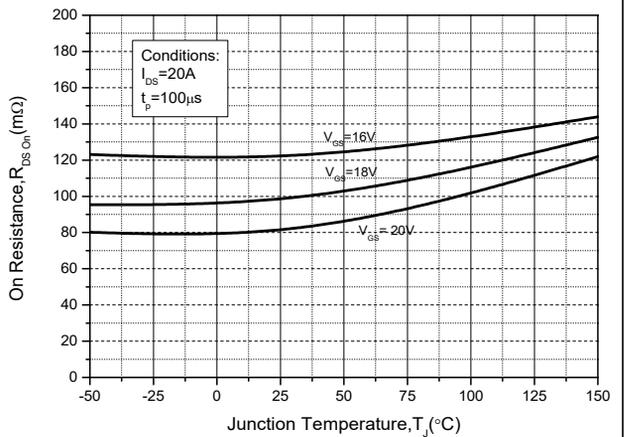


Figure 4. On-Resistance For Various Gate Voltage

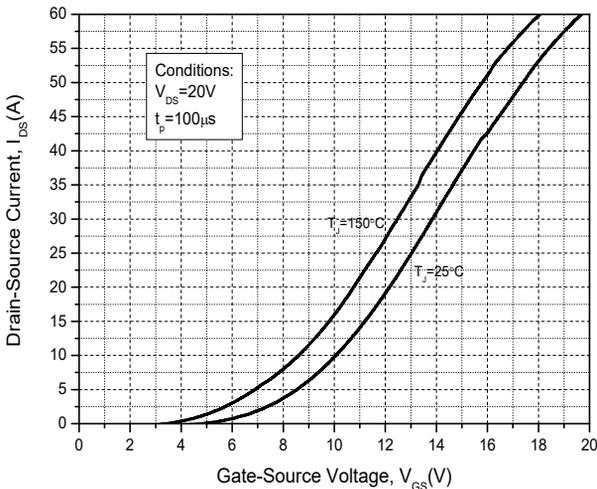


Figure 5. Transfer Characteristic for Various Junction Temperatures

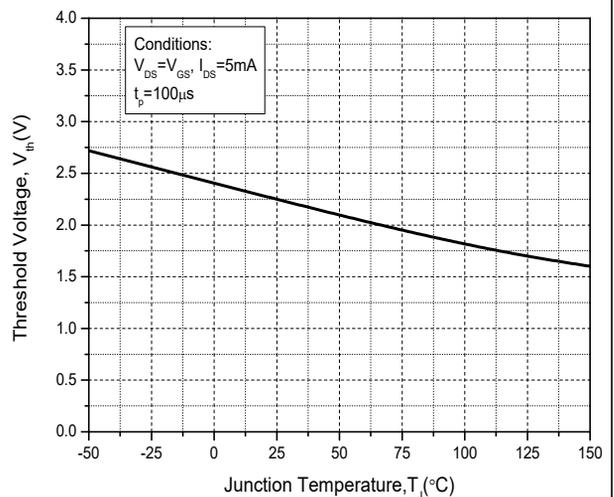


Figure 6. Threshold Voltage vs. Temperature

### Typical Characteristics (Continued)

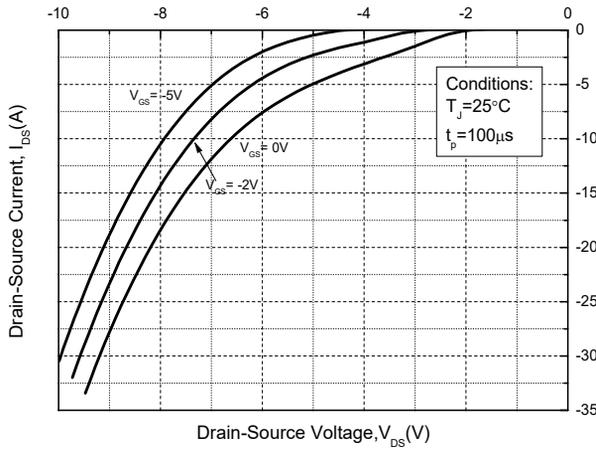


Figure 7. Body Diode Characteristics

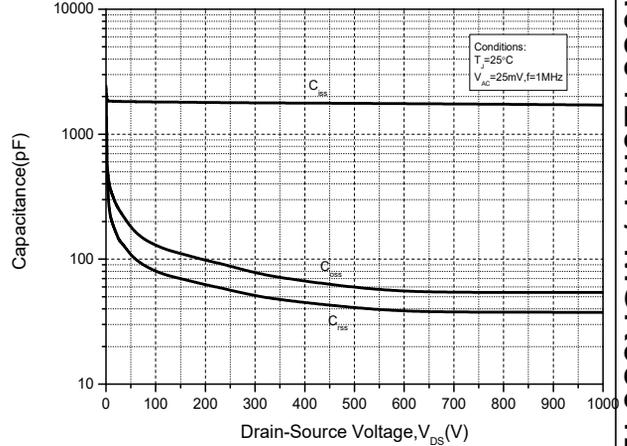


Figure 8. Capacitances vs. Drain-Source Voltage

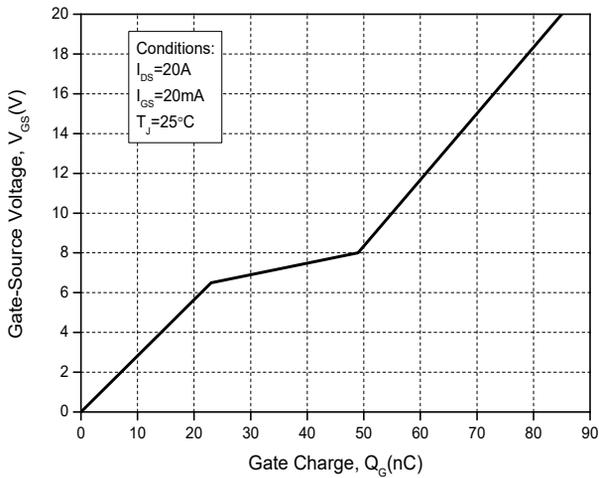


Figure 9. Gate Charge Characteristics

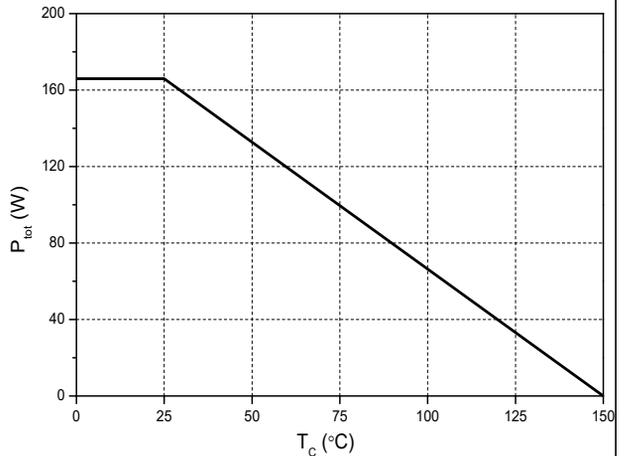


Figure 10. Power Dissipation Derating

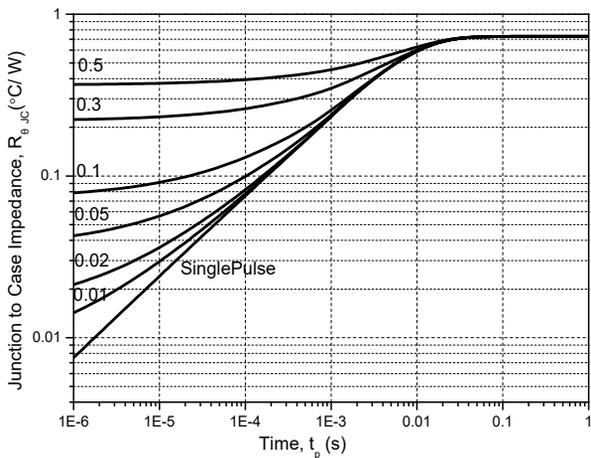
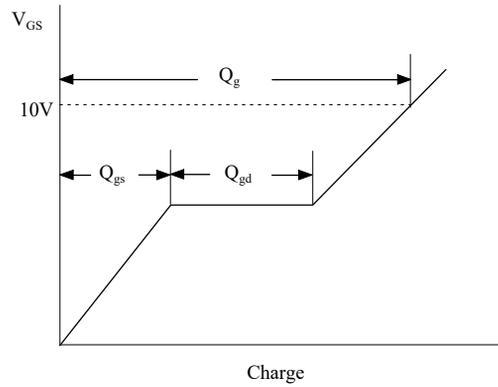
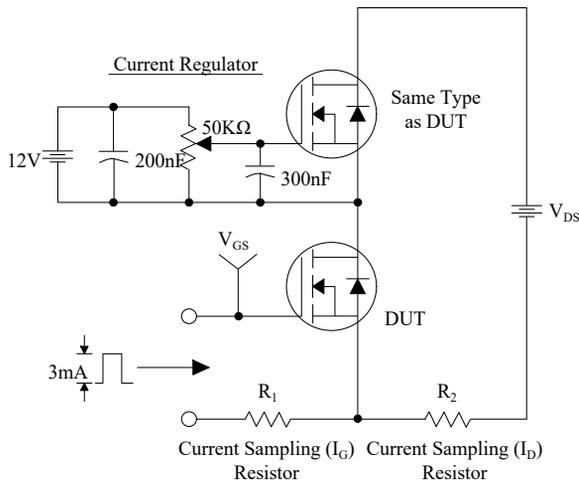
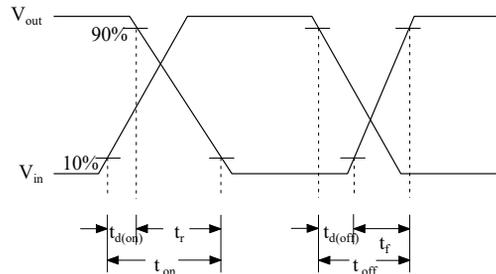
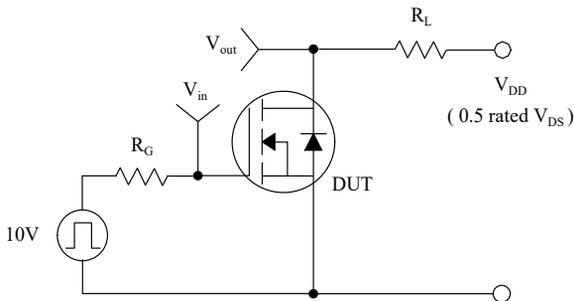


Figure 11. Transient Thermal Impedance

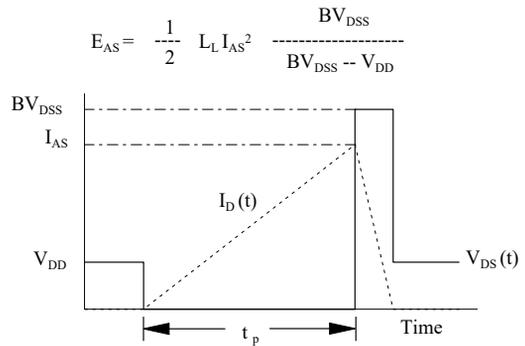
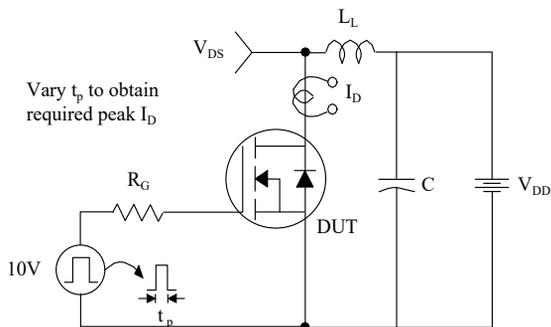
### Gate Charge Test Circuit & Waveform



### Resistive Switching Test Circuit & Waveforms



### Unclamped Inductive Switching Test Circuit & Waveforms



## Peak Diode Recovery dv/dt Test Circuit & Waveforms

